

# SYSMAC CJ-Series Analog I/O Unit

# CJ1W-AD/DA/MAD

CSM\_CJ1W-AD\_DA\_MAD\_DS\_E\_2\_1

## Consistent Microsecond Throughput: Models with Direct Conversion Join the Lineup

- Analog Input Units for converting analog input signals into binary data
- Analog Output Units for converting binary data into analog signals



CJ1W-AD042 **NEW**

CJ1W-DA042V **NEW**

## Features

### Analog Input Units

- Input up to eight analog signals with one Unit.
- Functions include line disconnection detection, averaging, peak value holding, offset/gain adjustment, and scaling. (Offset/gain adjustment is not supported by the CJ1W-AD042. Scaling is supported only by the CJ1W-AD042.)
- High-speed A/D conversion in 20  $\mu$ s/point with direct conversion function \* (CJ1W-AD042 only).

### Analog Output Units

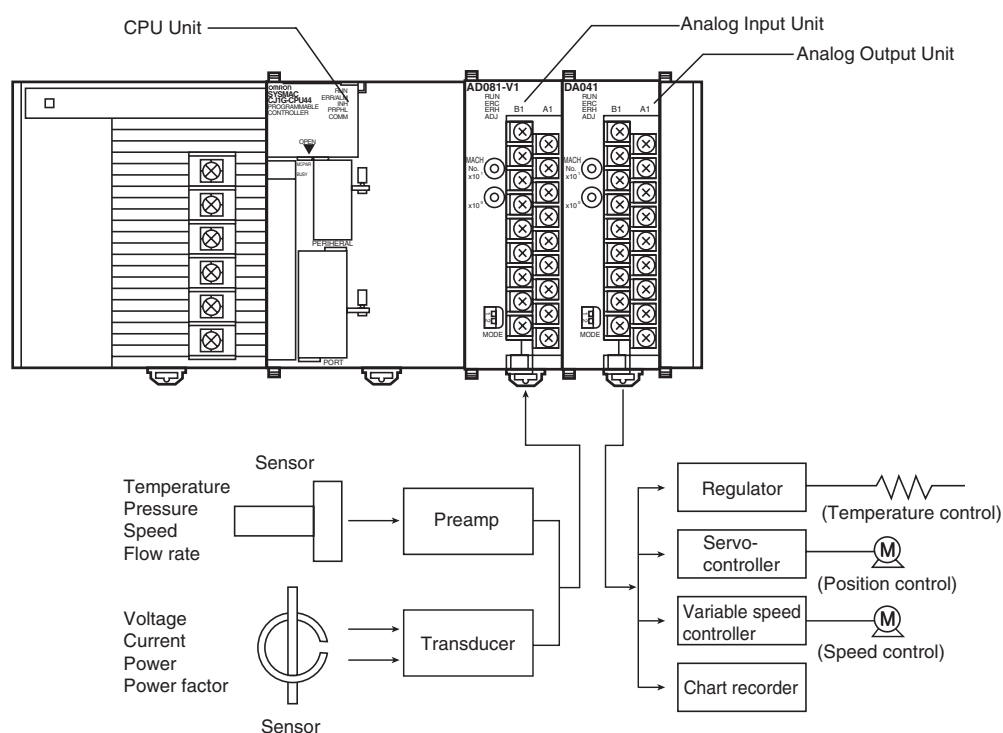
- Output up to eight analog signals with one Unit.
- Functions include output holding, offset/gain adjustment, and scaling. (Offset/gain adjustment is not supported by the CJ1W-DA042V. Scaling is supported only by the CJ1W-DA08V/DA08C/DA042V.)
- High-speed D/A conversion in 20  $\mu$ s/point with direct conversion function \* (CJ1W-DA042V only).

### Analog I/O Units

- Input up to four analog signals and output up to two analog signals with one Unit.
- Functions include line disconnection detection, input averaging, scaling, input peak value holding, output holding, ratio conversion, and offset/gain adjustment.

\* Direct Conversion Instructions for High-speed Units can be used to create a consistent response time from input through data processing and output.

## System Configuration





**Note:** The above diagram is an installation example for the CJ1W-AD081-V1 Analog Input Units and CJ1W-DA041 Analog Output Units.

## Ordering Information

### International Standards

- The standards are abbreviated as follows: U: UL, U1: UL (Class I Division 2 Products for Hazardous Locations), C: CSA, UC: cULus, UC1: cULus (Class I Division 2 Products for Hazardous Locations), CU: cUL, N: NK, L: Lloyd, and CE: EC Directives.
- Contact your OMRON representative for further details and applicable conditions for these standards.



### Analog Input Units

Unit type	Product name	I/O points	Signal range selection	Signal range	Resolution	Conversion Period	Accuracy at ambient temperature of 25°C	External connection	No. of unit numbers allocated	Current consumption (A)		Model	Standards
										5 V	24 V		
CJ1 Special I/O Units	Analog Input Unit 	4 inputs	Set separately for each input	1 to 5 V (1/10,000), 0 to 10 V (1/20,000), -5 to 5 V (1/20,000), -10 to 10 V (1/40,000), and 4 to 20 mA (1/10,000)		20 μs/1 point, 25 μs/2 points, 30 μs/3 points, 35 μs/4 points	Voltage: ±0.2% of F.S.  Current: ±0.4% of F.S.	Removable terminal block	1	0.52	---	<b>CJ1W-AD042</b>	UC1, CE
	Analog Input Units 	8 inputs		1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA	1/4,000 (Settable to 1/8,000) *1	1 ms/point (250 μs/point can also be set.) *1	Voltage: ±0.2% of F.S. Current: ±0.4% of F.S. *2			0.42	---	<b>CJ1W-AD081-V1</b>	UC1, N, L, CE
		4 inputs										<b>CJ1W-AD041-V1</b>	

\*1 The resolution and conversion speed cannot be set independently. If the resolution is set to 1/4,000, then the conversion speed will be 1 ms/point.

\*2 At 23 ±2°C


### Analog Output Units

Unit type	Product name	I/O points	Signal range selection	Signal range	Resolution	Conversion Period	Accuracy at ambient temperature of 25°C	External connection	External power supply	No. of unit numbers allocated	Current consumption (A)		Model	Standards
											5 V	24 V		
CJ1 Special I/O Units	Analog Output Unit <div>High-speed type</div> 	4 outputs	Set separately for each input	1 to 5 V (1/10,000), 0 to 10 V (1/20,000), and -10 to 10 V (1/40,000)		20 μs/ 1 point, 25 μs/ 2 points, 30 μs/ 3 points, 35 μs/ 4 points	±0.3% of F.S.	Removable terminal block	---	1	0.40	---	<b>CJ1W-DA042V</b> <sup>NEW</sup>	UC1, CE
	Analog Input Units 	8 outputs		1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V	1/4,000 (Settable to 1/8,000) *1	1 ms/point (Settable to 250 μs/ point) *1	±0.3% of F.S.		24 VDC +10% -15% , 140 mA max.		0.14 *2	<b>CJ1W-DA08V</b>	UC1, N, L, CE	
		8 outputs		4 to 20 mA					24 VDC +10% -15% , 170 mA max.		0.17 *2	<b>CJ1W-DA08C</b>	UC1, N, CE	
		4 outputs		1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA	1/4,000	1 ms/ point	Voltage: ±0.3% of F.S. Current: ±0.5% of F.S.		24 VDC +10% -15% , 200 mA max.		0.12	0.2 *2	<b>CJ1W-DA041</b>	UC1, N, L, CE
		2 outputs							24 VDC +10% -15% , 140 mA max.			0.14 *2	<b>CJ1W-DA021</b>	

\*1 The resolution and conversion speed cannot be set independently. If the resolution is set to 1/4,000, the conversion speed will be 1 ms/point.

\*2 This is for an external power supply, and not for internal current consumption.

## Analog I/O Units

Unit type	Product name	I/O points	Signal range selection	Signal range	Resolution	Conversion Period	Accuracy at ambient temperature of 25°C	External connection	No. of unit numbers allocated	Current consumption (A)		Model	Standards
										5 V	24 V		
CJ1 Special I/O Units		4 inputs	Set separately for each input	1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA	1/4,000 (Settable to 1/8,000)	1 ms/point (Settable to 500 µs/point)	Voltage: ±0.2% of F.S. Current: ±0.2% of F.S.  Voltage: ±0.3% of F.S. Current: ±0.3% of F.S.	Removable terminal block	1	0.58	—	CJ1W-MAD42	UC1, N, L, CE
		2 outputs											

**Note:** The resolution and conversion speed cannot be set independently. If the resolution is set to 1/4,000, then the conversion speed will be 1 ms/point.

## Accessories

Model	Accessories
CJ1W-AD081-V1/AD041-V1 CJ1W-DA08V/DA08C/DA041/DA021 CJ1W-DA042V CJ1W-MAD42	None.
CJ1W-AD042	Four jumpers (For a current input, a jumper is used to connect the current input positive terminal and the voltage input positive terminal.)

## Mountable Racks

Model	CJ system (CJ1, CJ2)		CP1H system	NSJ system	
	CPU Rack	Expansion Backplane	CP1H PLC	NSJ Controller	Expansion Backplane
CJ1W-AD042	8 Units *1	9 Units *2 (per Expansion Backplane)	2 Units *3	Not supported	9 Units *2 (per Expansion Backplane)
CJ1W-AD081-V1	10 Units *1	10 Units *2 (per Expansion Backplane)			10 Units *2 (per Expansion Backplane)
CJ1W-AD041-V1					
CJ1W-DA042V					
CJ1W-DA08V					
CJ1W-DA08C					
CJ1W-DA041					
CJ1W-DA021					
CJ1W-MAD42	7 Units *1	8 Units *2 (per Expansion Backplane)			8 Units *2 (per Expansion Backplane)

**Note:** It may not be possible to mount this many Units to a Rack depending on the current consumption of the other Units.

\*1 This is the number of Units for a CJ2H-CPU6 CJ2H CPU Unit (without EtherNet/IP) and a CJ1W-PA205 or CJ1W-PD025 Power Supply Unit.

\*2 This is the number of Units for a CJ1W-PA205 or CJ1W-PD025 Power Supply Unit.

\*3 A CP1W-EXT01 CJ Unit Adaptor is required.

## Individual Specifications

### Analog Input Units CJ1W-AD041-V1/AD081-V1/AD042

#### Specifications

Item			CJ1W-AD041-V1	CJ1W-AD081-V1	CJ1W-AD042	
Unit type			CJ-series Special I/O Unit			
Isolation *1			Between I/O and PLC signals: Photocoupler (No isolation between I/O signals.)		Between I/O and PLC signals: Digital isolator (No isolation between I/O signals.)	
External terminals			18-point detachable terminal block (M3 screws)			
Power consumption			420 mA max. at 5 VDC		520 mA max. at 5 VDC	
Dimensions (mm)			31 × 90 × 65 mm (W × H × D)			
Weight			140 g max.		150 g max.	
General specifications			Conforms to general specifications for SYSMAC CJ Series.			
Input specifications	Number of analog inputs		4	8	4	
	Input signal range *2		1 to 5 V 0 to 5 V 0 to 10 V −10 to 10 V 4 to 20 mA *3		1 to 5 V 0 to 10 V −5 to 5 V −10 to 10 V 4 to 20 mA *4	
	Maximum rated input (for 1 point) *5		Voltage Input: ±15 V Current Input: ±30 mA			
	Input impedance		Voltage Input: 1 MΩ min.    Current Input: 250 Ω (rated value)			
	Resolution		4,000/8,000 *6		1 to 5 V	10,000
					0 to 10 V	20,000
					−5 to 5 V	20,000
					−10 to 10 V	40,000
					4 to 20 mA	10,000
	Converted output data		16-bit binary data			
	Accuracy *7	25°C *8	Voltage Input: ±0.2% of F.S.Current Input: ±0.4% of F.S.			
		0°C to 55°C	Voltage Input: ±0.4% of F.S.Current Input: ±0.6% of F.S.			
	A/D conversion period *9		1 ms/250 μs *6		20 μs/1 point, 25 μs/2 points, 30 μs/3 points, 35 μs/4 points	
Input functions	Mean value processing		Stores the last "n" data conversions in the buffer, and stores the mean value of the conversion values. Buffer number: n = 2, 4, 8, 16, 32, 64		Stores the last "n" data conversions in the buffer, and stores the mean value of the conversion values. Buffer number: n = 2, 4, 8, 16, 32, 64, 128, 256, 512	
	Peak value holding		Stores the maximum conversion value while the Peak Value Hold Bit is ON.			
	Scaling		---		Setting values in any specified unit within a range of ±32,000 as the upper and lower limits allows A/D conversion to be executed and analog signals to be output with these values as full scale.	
	Input disconnection detection		Detects the disconnection and turns ON the Disconnection Detection Flag. *10			
	Offset/gain adjustment		Supported			
	Direct conversion		---		A/D conversion is performed and the converted value is refreshed when the ANALOG INPUT DIRECT CONVERSION instruction (AIDC) is executed. This instruction is supported by the CJ2□-CPU□□(-EIP) CPU Units with unit version 1.1 or later. CJ1 and CP1H CPU Units and NSJ Controllers do not support direct conversion.	

\*1 Do not apply a voltage higher than 600 V to the terminal block when performing withstand voltage test on this Unit. Otherwise, internal elements may deteriorate.

\*2 Input signal ranges can be set for each input.

\*3 Voltage input or current input are chosen by using the voltage/current switch at the back of the terminal block.

\*4 To use a current input, connect the positive current input terminal and positive voltage input terminal with the enclosed short bar.

\*5 The Analog Input Unit must be operated according to the input specifications provided here. Operating the Unit outside these specifications will cause the Unit to malfunction.

\*6 The resolution can be set to 8,000 and the conversion period to 250 μs in the DM Area (m+18). There is only one setting for both of these, i.e., they are both enabled or disabled together.

\*7 The accuracy is given for full scale. For example, an accuracy of ±0.2% means a maximum error of ±8 (BCD) at a resolution of 4,000. For the CJ1W-AD041-V1/ AD081-V1, the default setting is adjusted for voltage input. To use current input, perform the offset and gain adjustments as required.

\*8 For the CJ1W-AD041-V1/ AD081-V1, 23±2°C.

\*9 The A/D conversion period is the time required from when the Analog Input Unit receives the analog signal until it stores the converted value in internal memory. It takes at least one cycle for the converted data to be stored in the CPU Unit. (The direct conversion function of the CJ1W-AD042 is can be used to input data immediately to the CPU Unit.)

\*10 Line disconnection detection is supported only when the range is set to 1 to 5 V or 4 to 20 mA. If there is no input signal when the 1 to 5 V or 4 to 20 mA range is set, the Line Disconnection Flag will turn ON.

# Analog Output Units CJ1W-DA021/DA041/DA08V/DA08C/DA042V

## Specifications

Item			CJ1W-DA021	CJ1W-DA041	CJ1W-DA08V	CJ1W-DA08C	CJ1W-DA042V	
Unit type			CJ-series Special I/O Unit					
Isolation *1			Between I/O and PLC signals: Photocoupler (No isolation between I/O signals.)				Between outputs and PLC signals: Digital isolator (No isolation between output signals.)	
External terminals			18-point detachable terminal block (M3 screws)					
Power consumption			5 VDC, 120 mA max.		5 VDC, 140 mA max.		5 VDC, 400 mA max.	
External power supply *2			24 VDC <sup>+10%</sup> <sub>-15%</sub> (inrush current: 20 A min., pulse width: 1 ms min)				---	
			140 mA max.	200 mA max.	140 mA max.	170 mA max.	---	
Dimensions (mm)			31 × 90 × 65 mm (W × H × D)					
Weight			150 g max.					
General specifications			Conforms to general specifications for SYSMAC CJ-series Series.					
Output specifications	Number of analog outputs		2	4	8	8	4	
	Output signal range *3		1 to 5 V/4 to 20 mA 0 to 5 V 0 to 10 V -10 to 10 V		1 to 5 V 0 to 5 V 0 to 10 V -10 to 10 V		4 to 20 mA	1 to 5 V 0 to 10 V -10 to 10 V
	Output impedance		0.5 Ω max. (for voltage output) XXXX		0.5 Ω max. (for voltage output)		---	0.5 Ω max. (for voltage output)
	Max. output current (for 1 point)		12 mA (for voltage output) XXXX		2.4 mA (for voltage output)		---	2 mA (for voltage output)
	Maximum permissible load resistance		600 Ω (current output)		---		350 Ω	---
	Resolution		4,000		4,000/8,000 *8		1 to 5 V	10,000
							0 to 10 V	20,000
							-10 to 10 V	40,000
	Set data		16-bit binary data					
	Accuracy *4	25°C	Voltage output: ±0.3% of F.S. Current output: ±0.5% of F.S.			±0.3% of F.S.	±0.3% of F.S.	±0.3% of F.S.
0°C to 55°C		Voltage output: ±0.5% of F.S. Current output: ±0.8% of F.S.			±0.5% of F.S.	±0.6% of F.S.	±0.5% of F.S.	
D/A conversion period *5		1.0 ms per point			1.0 ms or 250 μs per point *8		20 μs/1 point, 25 μs/2 points, 30 μs/3 points, 35 μs/4 points	
Output functions	Output hold function		Outputs the specified output status (CLR, HOLD, or MAX) under any of the following circumstances. • When the Conversion Enable Bit is OFF. *6 • In adjustment mode, when a value other than the output number is output during adjustment. *7 • When output setting value error occurs or PLC operation stops. • When the CPU Unit is on standby. • When the Load is OFF.					
	Scaling		---		Supported only for a conversion period of 1 ms and resolution of 4,000. Setting values in any specified unit within a range of ±32,000 as the upper and lower limits allows D/A conversion to be executed and analog signals to be output with these values as full scale.		Setting values in any specified unit within a range of ±32,000 as the upper and lower limits allows D/A conversion to be executed and analog signals to be output with these values as full scale.	
	Offset/gain adjustment		Supported					
	Direct conversion		---				D/A conversion is performed and the output value is refreshed when the ANALOG OUTPUT DIRECT CONVERSION instruction (AODC) is executed. This instruction is supported by the CJ2□-CPU□□(-EIP) CPU Units with unit version 1.1 or later. CJ1 and CP1H CPU Units and NSJ Controllers do not support direct conversion.	

\*1 Do not apply a voltage higher than 600 V to the terminal block when performing withstand voltage test on this Unit.

\*2 The maximum number of Analog Output Units that can be mounted to one Rack varies depending on the current consumption of the other Units mounted to the Rack.

Select a 24 VDC power supply based on the surge current. The following OMRON external power supplies are recommended.

Manufacturer	Model number	Specifications
OMRON	S8VS-06024	100 to 240 VAC, 60 W
	S8VS-12024	100 to 240 VAC, 120 W
	S8VM-05024	100 to 240 VAC, 50 W
	S8VM-10024	100 to 240 VAC, 100 W

\*3 Output signal ranges can be set for each output.

- \*4 The accuracy is given for full scale. For example, an accuracy of  $\pm 0.3\%$  means a maximum error of  $\pm 60$  mV for a  $-10$  to  $10$  V range. For the CJ1W-DA021/041, the accuracy is at the factory setting for a current output. When using a voltage output, adjust the offset gain as required.
- \*5 The D/A conversion period is the time required for the Analog Output Unit to convert and output the data that was received from the CPU Unit. It takes at least one cycle for the data stored in the CPU Unit to be read by the Analog Output Unit. (The direct conversion function of the CJ1W-DA042V can be used to output data immediately from the CPU Unit.)
- \*6 When the operation mode for the CPU Unit is changed from RUN mode or MONITOR mode to PROGRAM mode, or when the power is turned ON, the Output Conversion Enable Bit will turn OFF. The output status specified according to the output hold function will be output.
- \*7 The CJ1W-DA042V does not have an Adjustment Mode.
- \*8 The CJ1W-DA08V/08C can be set to a conversion cycle of  $250\ \mu\text{s}$  and a resolution of 8,000 using the setting in D (m+18).

## Analog I/O Unit CJ1W-MAD42

### Specifications

Item	CJ1W-MAD42
Unit type	CJ-series Special I/O Unit
Isolation	Between I/O and PLC signals: Photocoupler (No isolation between I/O signals.)
External terminals	18-point detachable terminal block (M3 screws)
Current consumption	580 mA max. at 5 V DC
Dimensions (mm)	$31 \times 90 \times 65$ mm (W $\times$ H $\times$ D)
Weight	150 g max.
General specifications	Conforms to general specifications for SYSMAC CJ-series Series.

### Input Specifications and Functions

Item	Voltage input	Current input
Number of analog inputs	4	
Input signal range *1	1 to 5 V 0 to 5 V 0 to 10 V $-10$ to $10$ V	4 to 20 mA *2
Maximum rated input (for 1 point) *3	$\pm 15$ V	$\pm 30$ mA
Input impedance	1 M $\Omega$ min.	250 $\Omega$ (rated value)
Resolution	4,000/8,000 *7	
Converted output data	16-bit binary data	
Accuracy *4	25°C	$\pm 0.2\%$ of F.S.
	0°C to 55°C	$\pm 0.4\%$ of F.S.
A/D conversion period *5	1.0 ms/500 $\mu\text{s}$ per point *7	
Mean value processing	Stores the last "n" data conversions in the buffer, and stores the mean value of the conversion values. Buffer number: n = 2, 4, 8, 16, 32, 64	
Peak value holding	Stores the maximum conversion value while the Peak Value Hold Bit is ON.	
Scaling	Enabled only for conversion period of 1 ms and resolution of 4,000. Setting any values within a range of $\pm 32,000$ as the upper and lower limits allows the A/D conversion result to be output with these values as full scale.	
Input disconnection detection	Detects the disconnection and turns ON the Disconnection Detection Flag.	
Offset/gain adjustment	Supported	

## Output Specifications

Item		Voltage output	Current output
Number of analog outputs		2	
Output signal range *1		1 to 5 V 0 to 5 V 0 to 10 V -10 to 10 V	4 to 20 mA
Output impedance		0.5 Ω max.	—
Maximum external output current (for 1 point)		2.4 mA	—
Maximum allowed load resistance		—	600 Ω
Resolution		4,000/8,000 *7	
Set data		16-bit binary data	
Accuracy *4	25°C	±0.3% of F.S.	±0.3% of F.S.
	0°C to 55°C	±0.5% of F.S.	±0.6% of F.S.
D/A conversion period *5		1.0 ms/500 μs per point	
Output hold function		Outputs the specified output status (CLR, HOLD, or MAX) under any of the following circumstances. <ul style="list-style-type: none"> <li>• When the Conversion Enable Bit is OFF. *6</li> <li>• In adjustment mode, when a value other than the output number is output during adjustment.</li> <li>• When output setting value error occurs or PLC operation stops.</li> <li>• When the CPU Unit is on standby.</li> <li>• When the Load is OFF.</li> </ul>	
Scaling		Enabled only for conversion period or 1 ms and resolution of 4,000. Setting any values within a range of ±32,000 as the upper and lower limits allows D/A conversion to be executed and analog signals to be output with these values as full scale.	
Ratio conversion function *5		Stores the results of positive and negative gradient analog inputs calculated for ratio and bias as analog output values. Positive gradient: Analog output = A × Analog input + B (A: 0 to 99.99, B: 8000 to 7FFF hex) Negative gradient: Analog output = F - A × Analog input + B (A: 0 to 99.99, B: 8000 to 7FFF hex, F: Output range maximum value)	
Offset/gain adjustment		Supported	

\*1 Input and output signal ranges can be set for each input and output.

\*2 Voltage input or current input are chosen by using the voltage/current switch at the back of the terminal block.

\*3 The Analog I/O Unit must be operated according to the input specifications provided here. Operating the Unit outside these specifications will cause the Unit to malfunction.

\*4 The accuracy is given for full scale. For example, for an input, an accuracy of ±0.2% means a maximum error of ±8 (BCD) at a resolution of 4,000. For an output, an accuracy of ±0.3% means a maximum error of ±60 mV for a -10 to 10 V range.

\*5 The A/D conversion period is the time required from when the Analog Input Unit receives the analog signal until it stores the converted value in internal memory. It takes at least one cycle for the converted data to be stored in the CPU Unit.

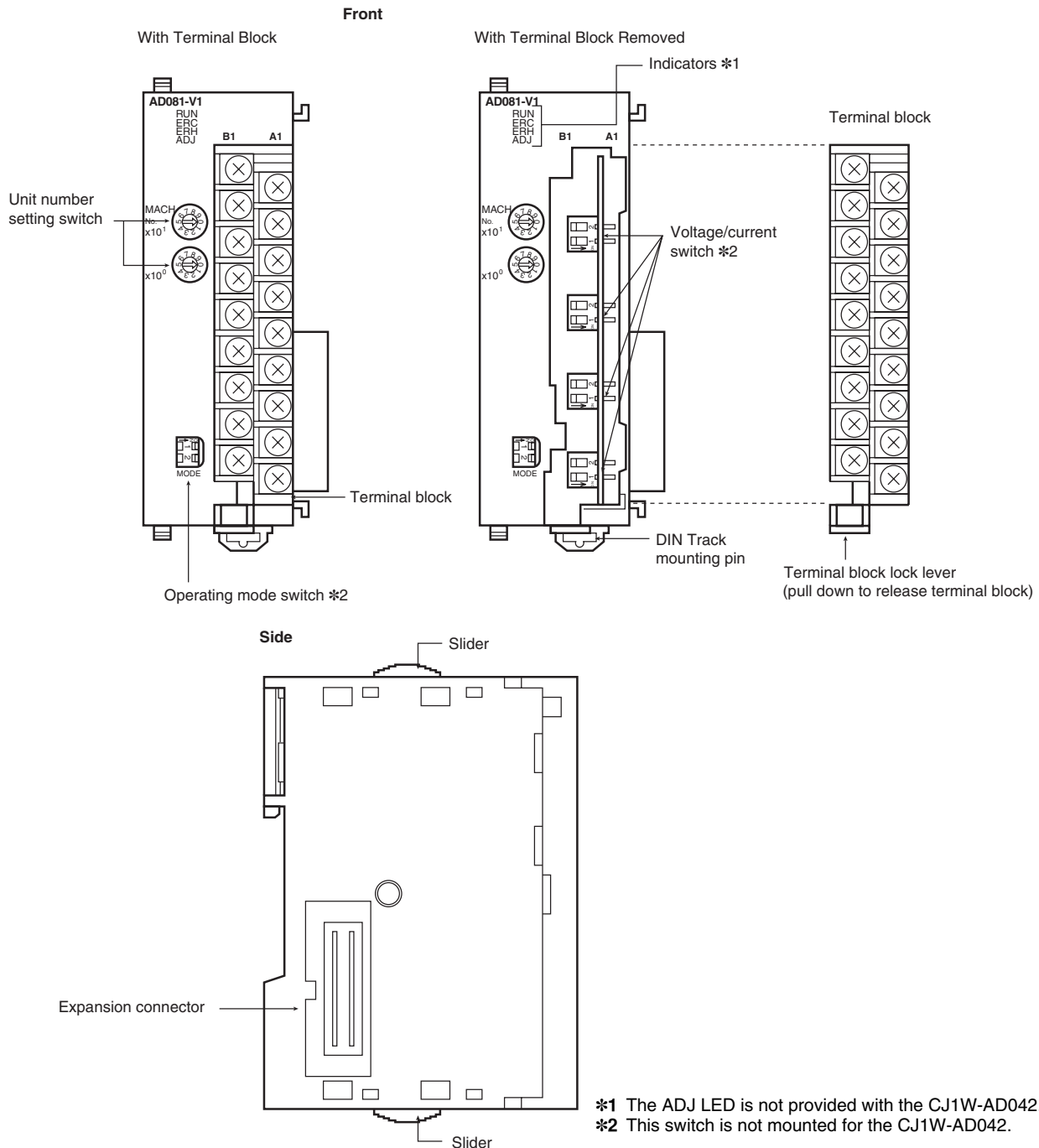
The D/A conversion period is the time required for the Analog Output Unit to convert and output the data that was received from the CPU Unit. It takes at least one cycle for the data stored in the CPU Unit to be read by the Analog Output Unit.

\*6 When the operation mode for the CPU Unit is changed from RUN mode or MONITOR mode to PROGRAM mode, or when the power is turned ON, the Output Conversion Enable Bit will turn OFF. The output status specified according to the output hold function will be output.

\*7 By means of the D (m+18) setting, the resolution can be changed to 8,000, and the conversion period can be changed to 500 μs.

## External Interface

### Analog Input Units CJ1W-AD041-V1/AD081-V1/AD042 Components



\*1 The ADJ LED is not provided with the CJ1W-AD042.

\*2 This switch is not mounted for the CJ1W-AD042.

## Indicators

The indicators show the operating status of the Unit. The following table shows the meanings of the indicators.

LED	Meaning	Indicator	Operating status
RUN (green)	Operating	Lit	Operating in normal mode.
		Not lit	Unit has stopped exchanging data with the CPU Unit.
ERC (red)	Error detected by Unit	Lit	Alarm has occurred (such as disconnection detection) or initial settings are incorrect.
		Not lit	Operating normally.
ERH (red)	Error in the CPU Unit	Lit	Error has occurred during data exchange with the CPU Unit.
		Not lit	Operating normally.
ADJ (yellow) *	Adjusting	Flashing	Operating in offset/gain adjustment mode.
		Not lit	Other than the above.

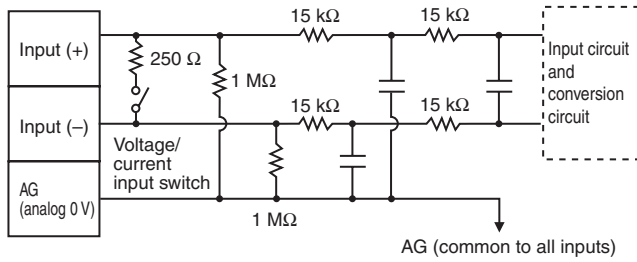
\* The ADJ LED is not provided with the CJ1W-AD042.



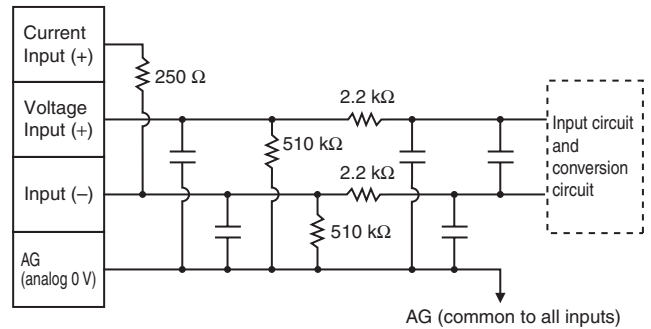
## Input Circuits

The following diagrams show the internal circuit of the analog input section.

### CJ1W-AD041-V1/AD081-V1



### CJ1W-AD042



## Terminal Arrangement

The signal names corresponding to the connecting terminals are as shown in the following diagram.

### CJ1W-AD041-V1

Input 2 (+)	B1	A1	Input 1 (+)
Input 2 (-)	B2	A2	Input 1 (-)
Input 4 (+)	B3	A3	Input 3 (+)
Input 4 (-)	B4	A4	Input 3 (-)
AG	B5	A5	AG
N.C.	B6	A6	N.C.
N.C.	B7	A7	N.C.
N.C.	B8	A8	N.C.
N.C.	B9	A9	N.C.

### CJ1W-AD081-V1

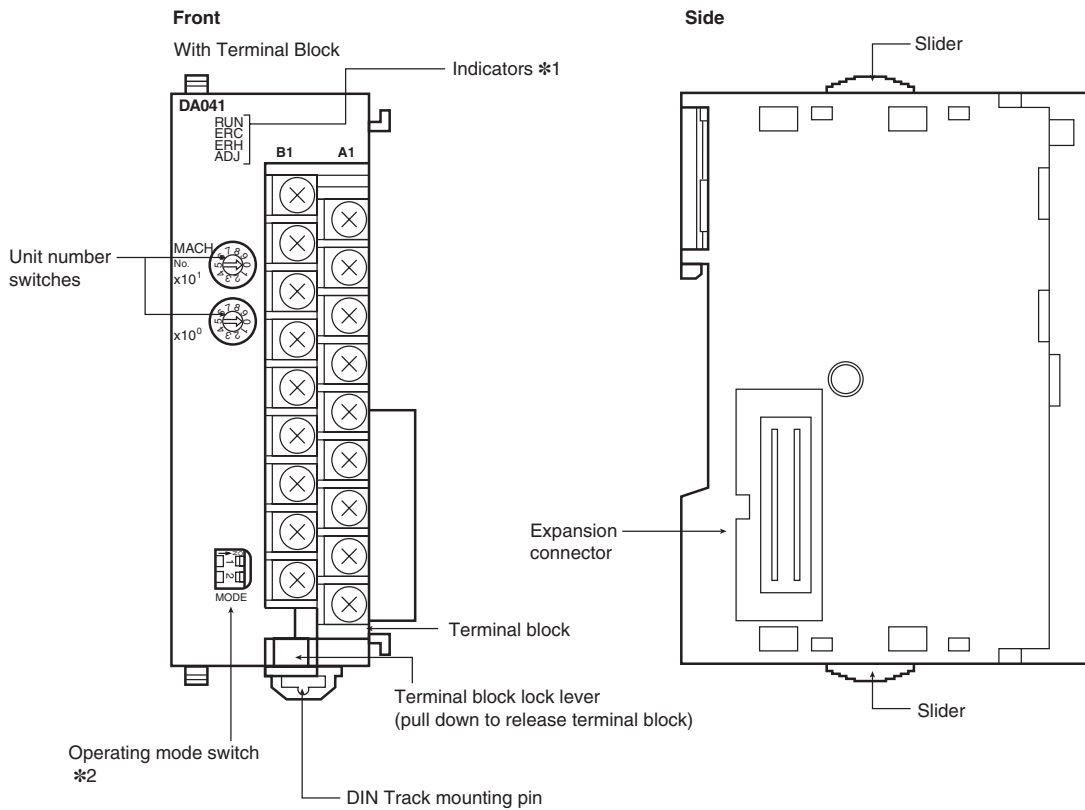
Input 2 (+)	B1	A1	Input 1 (+)
Input 2 (-)	B2	A2	Input 1 (-)
Input 4 (+)	B3	A3	Input 3 (+)
Input 4 (-)	B4	A4	Input 3 (-)
AG	B5	A5	AG
Input 6 (+)	B6	A6	Input 5 (+)
Input 6 (-)	B7	A7	Input 5 (-)
Input 8 (+)	B8	A8	Input 7 (+)
Input 8 (-)	B9	A9	Input 7 (-)

### CJ1W-AD042

Current Input 2 (+)	B1	A1	Current Input 1 (+)
Voltage Input 2 (+)	B2	A2	Voltage Input 1 (+)
Input 2 (-)	B3	A3	Input 1 (-)
AG	B4	A4	AG
Current Input 4 (+)	B5	A5	Current Input 3 (+)
Voltage Input 4 (+)	B6	A6	Voltage Input 3 (+)
Input 4 (-)	B7	A7	Input 3 (-)
AG	B8	A8	AG
N.C.	B9	A9	N.C.

- Note:**
1. The analog input numbers that can be used are set in the Data Memory (DM).
  2. The input signal ranges for each input are set in the Data Memory (DM). They can be set in units of input numbers.
  3. The AG terminals are connected to the 0 V analog circuit in the Unit. Connecting shielded input lines can improve noise resistance.
  4. Do not connect anything to NC terminals.
  5. To use a current input with the CJ1W-AD042, connect the positive current input terminal and positive voltage input terminal with the enclosed short bar.
  6. Connect a surge suppressor to inductive loads in the system (e.g., magnetic contactors, relays, and solenoids).

## Analog Output Units CJ1W-DA021/041/08V/08C/DA042V Components



\*1 The ADJ LED is not provided with the CJ1W-DA042V.

\*2 This switch is not mounted for the CJ1W-DA08V, CJ1W-DA08C and CJ1W-DA042V.

### Indicators

The indicators show the operating status of the Unit. The following table shows the meanings of the indicators.

LED	Meaning	Indicator	Operating status
RUN (green)	Operating	Lit	Operating in normal mode.
		Not lit	Unit has stopped exchanging data with the CPU Unit.
ERC (red)	Error detected by Unit	Lit	Alarm has occurred (such as disconnection detection) or initial settings are incorrect.
		Not lit	Operating normally.
ERH (red)	Error in the CPU Unit	Lit	Error has occurred during data exchange with the CPU Unit.
		Not lit	Operating normally.
ADJ (yellow) *	Adjusting	Flashing	Operating in offset/gain adjustment mode.
		Not lit	Other than the above.

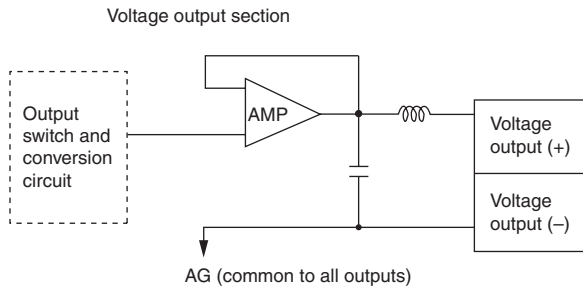
\* The ADJ LED is not provided with the CJ1W-DA042V.

## Output Circuits

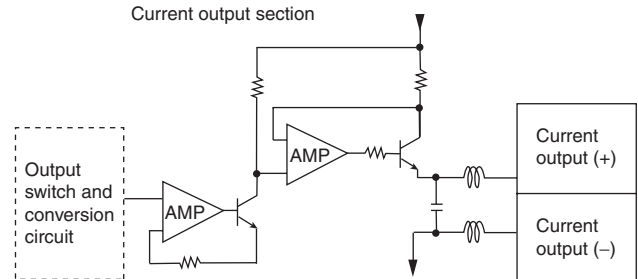
The following diagrams show the internal circuit of the analog output section.

### CJ1W-DA021/DA041/DA08V/DA08C

#### Voltage Output Circuits

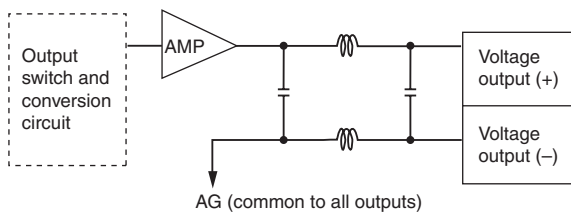


#### Current Output Circuits



### CJ1W-DA042V

#### Voltage Output Circuits



## Terminal Arrangement

The signal names corresponding to the connecting terminals are as shown in the following diagram.

#### CJ1W-DA021

Voltage output 2 (+)	B1	A1	Voltage output 1 (+)
Output 2 (-)	B2	A2	Output 1 (-)
Current output 2 (+)	B3	A3	Current output 1 (+)
N.C.	B4	A4	N.C.
N.C.	B5	A5	N.C.
N.C.	B6	A6	N.C.
N.C.	B7	A7	N.C.
N.C.	B8	A8	N.C.
0 V	B9	A9	24 V

#### CJ1W-DA041

Voltage output 2 (+)	B1	A1	Voltage output 1 (+)
Output 2 (-)	B2	A2	Output 1 (-)
Current output 2 (+)	B3	A3	Current output 1 (+)
Voltage output 4 (+)	B4	A4	Voltage output 3 (+)
Output 4 (-)	B5	A5	Output 3 (-)
Current output 4 (+)	B6	A6	Current output 3 (+)
N.C.	B7	A7	N.C.
N.C.	B8	A8	N.C.
0 V	B9	A9	24 V

#### CJ1W-DA08V (Voltage Output) and CJ1W-DA08C (Current Output)

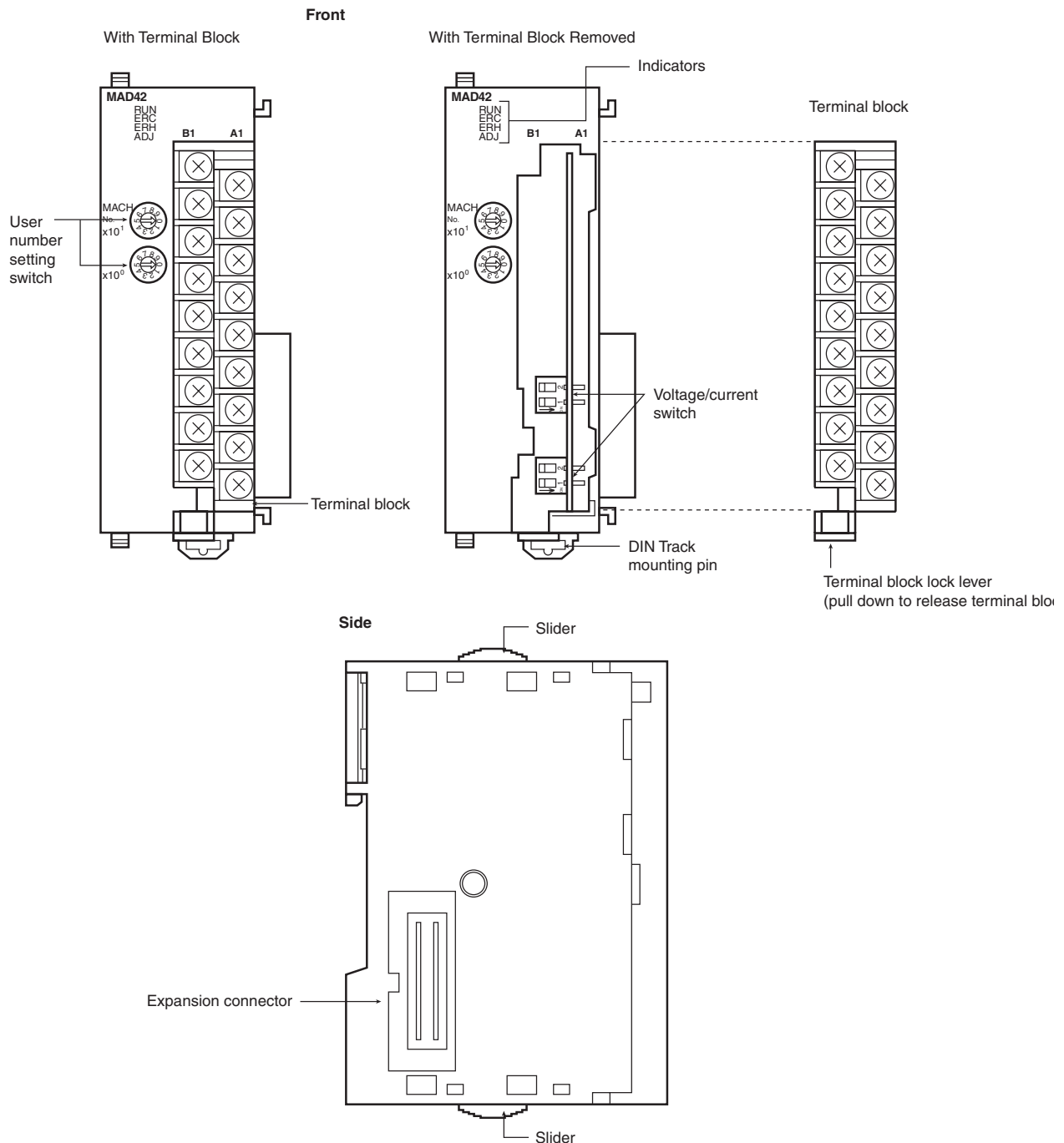
Output 2 (+)	B1	A1	Output 1 (+)
Output 2 (-)	B2	A2	Output 1 (-)
Output 4 (+)	B3	A3	Output 3 (+)
Output 4 (-)	B4	A4	Output 3 (-)
Output 6 (+)	B5	A5	Output 5 (+)
Output 6 (-)	B6	A6	Output 5 (-)
Output 8 (+)	B7	A7	Output 7 (+)
Output 8 (-)	B8	A8	Output 7 (-)
0 V	B9	A9	24 V

#### CJ1W-DA042V

Output 2 (+)	B1	A1	Output 1 (+)
Output 2 (-)	B2	A2	Output 1 (-)
N.C.	B3	A3	N.C.
Output 4 (+)	B4	A4	Output 3 (+)
Output 4 (-)	B5	A5	Output 3 (-)
N.C.	B6	A6	N.C.
N.C.	B7	A7	N.C.
N.C.	B8	A8	N.C.
N.C.	B9	A9	N.C.

- Note:**
1. The analog output numbers that can be used are set in the Data Memory (DM).
  2. The output signal ranges for each output are set in the Data Memory (DM). They can be set in units of output numbers.
  3. The N.C. terminals are not connected to internal circuit.
  4. Use a separate power supply from the one used for Basic I/O Units. Faulty Unit operation may be caused by noise if power is supplied from the same source. (This does not apply to CJ1W-DA042V.)
  5. Connect a surge suppressor to inductive loads in the system (e.g., magnetic contactors, relays, and solenoids).

## Analog I/O Unit CJ1W-MAD42 Components



### Indicators

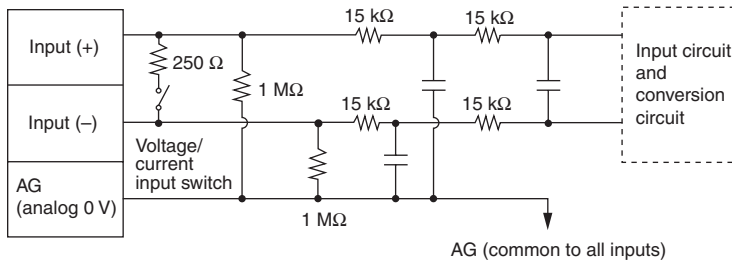
The indicators show the operating status of the Unit. The following table shows the meanings of the indicators.

LED	Meaning	Indicator	Operating status
RUN (green)	Operating	Lit	Operating in normal mode.
		Not lit	Unit has stopped exchanging data with the CPU Unit.
ERC (red)	Error detected by Unit	Lit	Alarm has occurred (such as disconnection detection) or initial settings are incorrect.
		Not lit	Operating normally.
ADJ (yellow)	Adjusting	Flashing	Operating in offset/gain adjustment mode.
		Not lit	Other than the above.
ERH (red)	Error in the CPU Unit	Lit	Error has occurred during data exchange with the CPU Unit.
		Not lit	Operating normally.

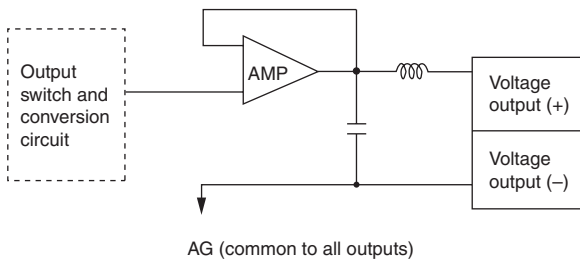
## I/O Circuit

The following diagrams show the internal circuit of the analog I/O section.

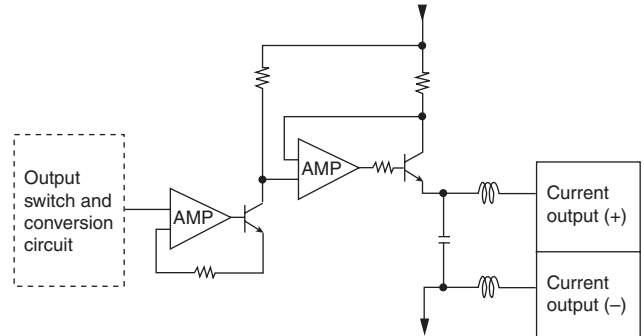
### Input Circuits



### Voltage Output Circuits



### Current Output Circuits



## Terminal Arrangement

The signal names corresponding to the connecting terminals are as shown in the following diagram.

### CJ1W-MAD42

Voltage output 2 (+)	B1	A1	Voltage output 1 (+)
Output 2 (-)	B2	A2	Output 1 (-)
Current output 2 (+)	B3	A3	Current output 1 (+)
N.C.	B4	A4	N.C.
Input 2 (+)	B5	A5	Input 1 (+)
Input 2 (-)	B6	A6	Input 1 (-)
AG	B7	A7	AG
Input 4 (+)	B8	A8	Input 3 (+)
Input 4 (-)	B9	A9	Input 3 (-)

- Note:**
1. The analog I/O numbers that can be used are set in the Data Memory (DM).
  2. The I/O signal ranges for each input and output are set in the Data Memory (DM). They can be set in units of I/O numbers.
  3. The AG terminal (A7, B7) is connected to the 0 V analog circuit in the Unit. Connecting shielded input lines can improve noise resistance.
  4. The N.C. terminals (A4, B4) are not connected to internal circuit.

## Wiring Vasic I/O Units with Terminal Blocks

### Crimp terminals

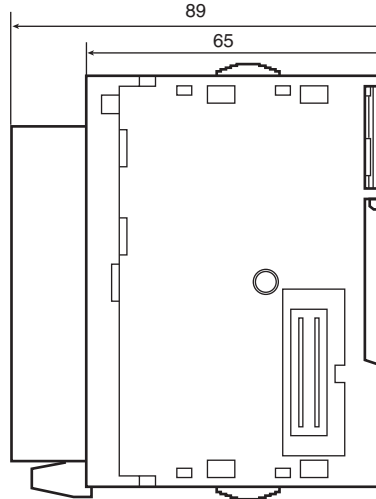
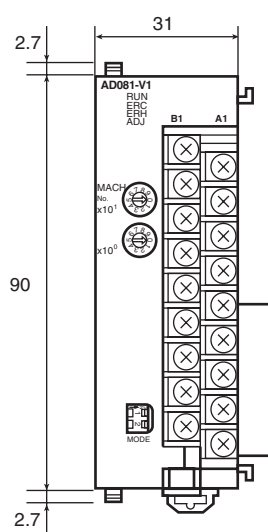
Use crimp terminals (M3) having the dimensions shown below.



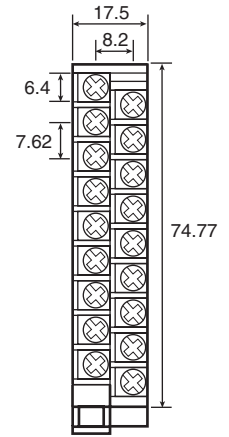
## Dimensions

(Unit: mm)

CJ1W-AD041-V1/081-V1/AD042  
CJ1W-DA021/041/08V/08C/DA042V  
CJ1W-MAD42



### Terminal Block Dimensions



**Note:** The appearance varies with the model.

## About Manuals

Cat. No	Model	Manual name	Contents
W345	SYSMAC CS/CJ Series CS1W-AD041-V1/081-V1/161 CS1W-DA041/08V/08C CS1W-MAD44 CJ1W-AD041-V1/081-V1 CJ1W-DA021/041/08V/08C CJ1W-MAD42	CS/CJ-series Analog I/O Units Operation Manual	Provides information on using the CS/CJ-series Analog Input, Analog Output, and Analog I/O Units.
W393	CJ1H-CPU□□H-R CJ1G/H-CPU□□H CJ1G-CPU□□P CJ1G-CPU□□ CJ1M-CPU□□	SYSMAC CJ Series Programmable Controllers Operation Manual	Provides an outlines of and describes the design, installation, maintenance, and other basic operations for the CJ-series PLCs.
W472	CJ2H-CPU6□-EIP CJ2H-CPU6□	CJ-series CJ2 CPU Unit Hardware User's Manual	Describes the following for CJ2 CPU Units: <ul style="list-style-type: none"> <li>• Overview and features</li> <li>• Basic system configuration</li> <li>• Part nomenclature and functions</li> <li>• Mounting and setting procedure</li> <li>• Remedies for errors</li> </ul> Also refer to the Software User's Manual (W473).
W474	CJ2H-CPU6□-EIP CJ2H-CPU6□ CS1G/H-CPU□□H CS1G/H-CPU□□-V1 CJ1G/H-CPU□□H CJ1G-CPU□□ CJ1M-CPU□□ NSJ□-□□□□(B)-G5D NSJ□-□□□□(B)-M3D	CS/CJ/NSJ-series Instructions Reference Manual	Describes each programming instruction in detail.